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Attorney Docket 0553-0184.01



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of)
Yamazaki et al.)
Serial No.: 10/642,803)
Filed: August 18, 2003)
For: Semiconductor Device And)
Manufacturing Method Thereof)
Art Unit: 2871)
Examiner: Andrew Schechter)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
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Shannon Wallace
Name of applicant, assignee, or Registered Rep.
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Signature Date

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ELECTION AND AMENDMENT C

Sir:

In response to the Restriction Requirement of September 10, 2004, Applicants elect to prosecute Group I: Claims 1 and 20-26 in the above-identified application. Applicants are making this election and canceling the non-elected claims without prejudice to filing a divisional application on the non-elected claims and without disclaimer.

Applicants are also adding new claims to prosecute in this application.

Therefore, please amend the above-identified application as follows: